

# Finite and Algorithmic Model Theory

## Lecture 1 (Dresden 12.10.22, Revised version)

Lecturer: Bartosz “Bart” Bednarczyk

TECHNISCHE UNIVERSITÄT DRESDEN & UNIWERSYTET WROCLAWSKI



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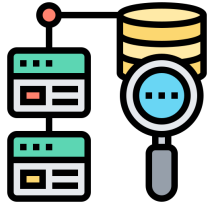
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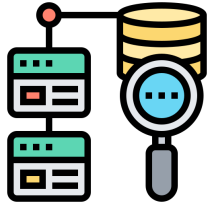
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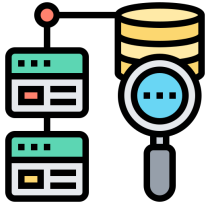
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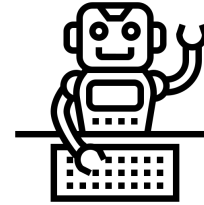
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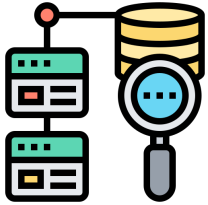




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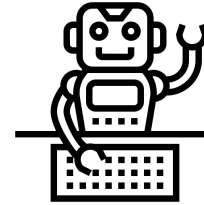
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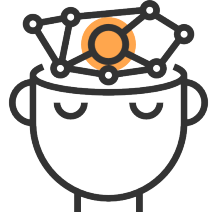
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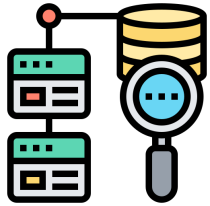
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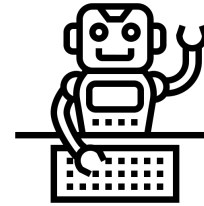
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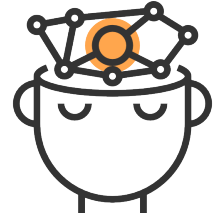
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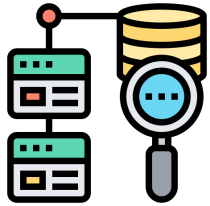


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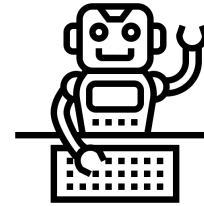
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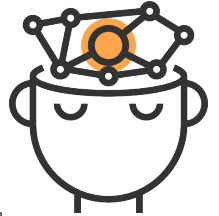
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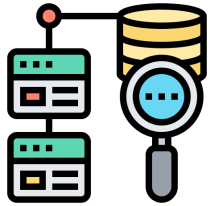
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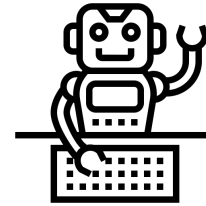
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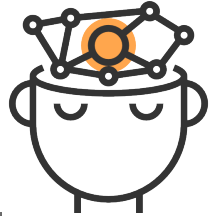
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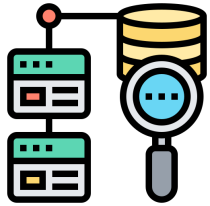
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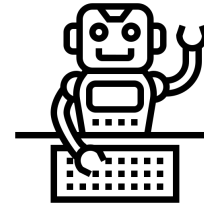
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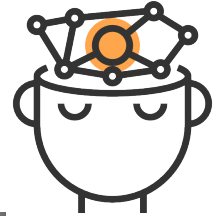
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**Feel free to ask questions and interrupt me!**

Don't be shy! If needed send me an email ([bartosz.bednarczyk@cs.uni.wroc.pl](mailto:bartosz.bednarczyk@cs.uni.wroc.pl)) or approach me after the lecture!

Reminder: this is an advanced lecture. Target: people that had fun learning logic during BSc studies!

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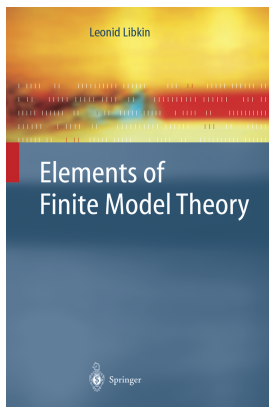
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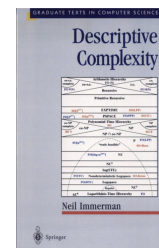
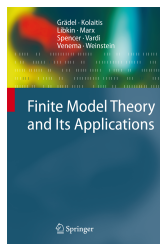
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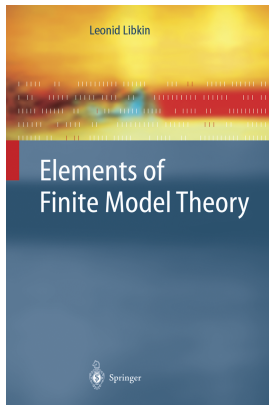


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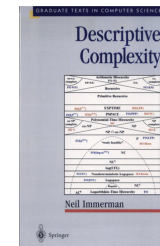
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**Last but Not Least: I offer MSc/PHD research projects for motivated students!**



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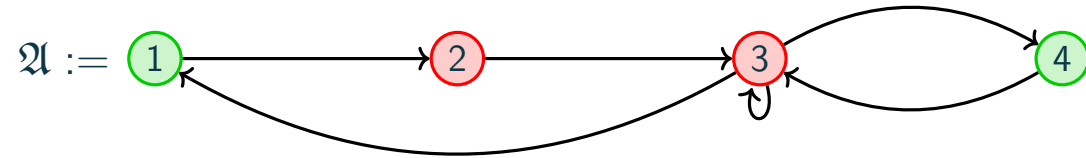
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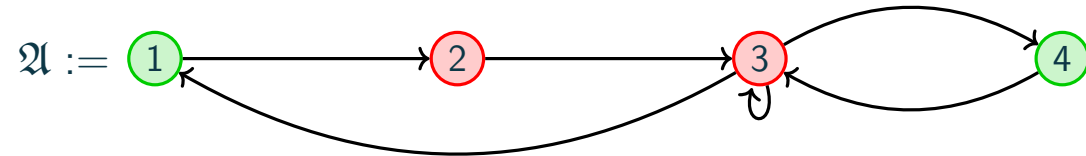


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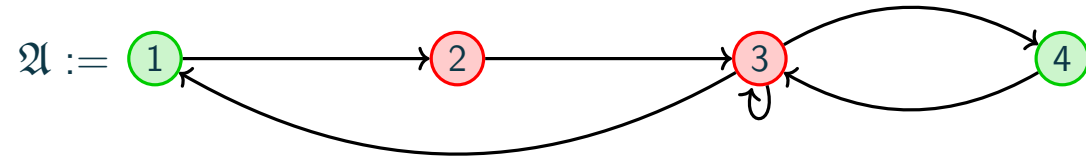
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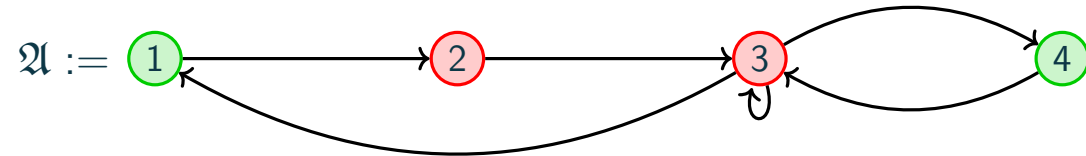
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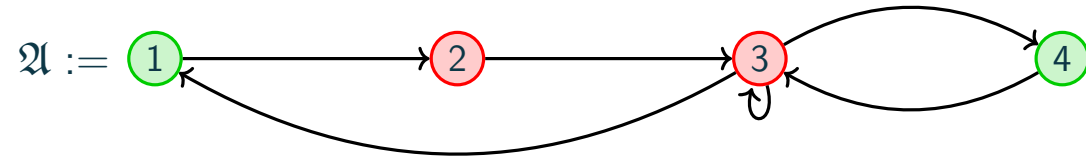
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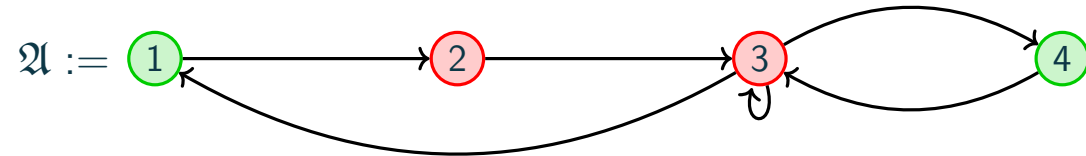
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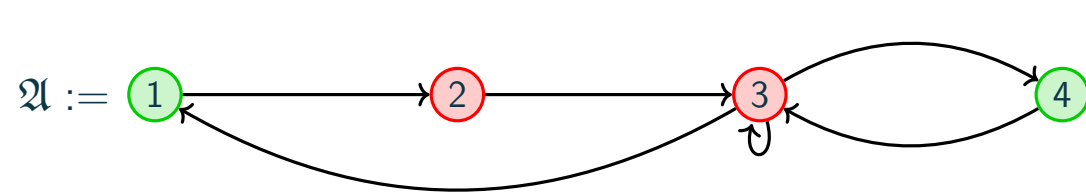
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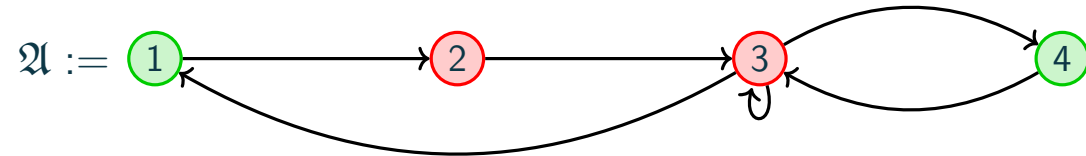
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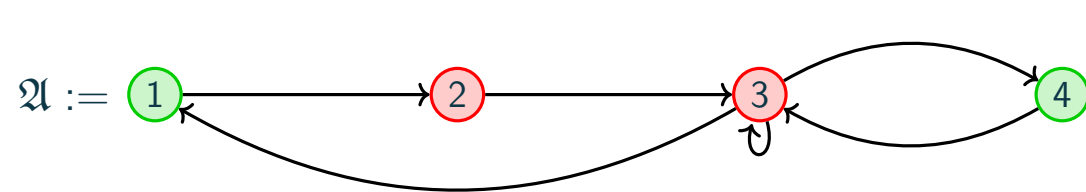
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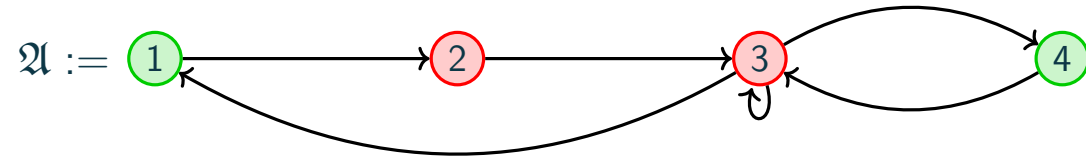
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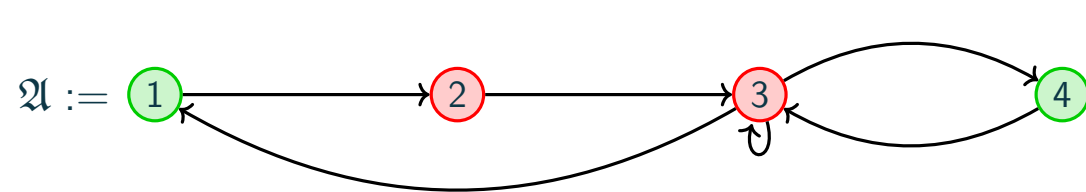
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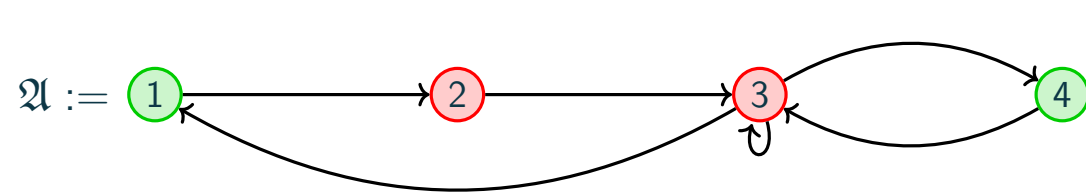
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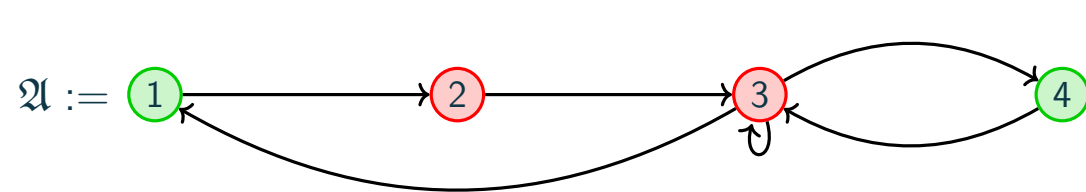
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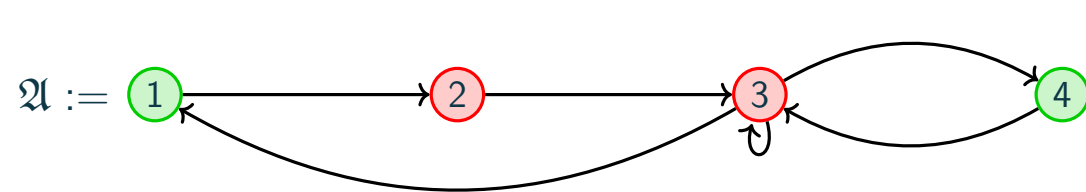
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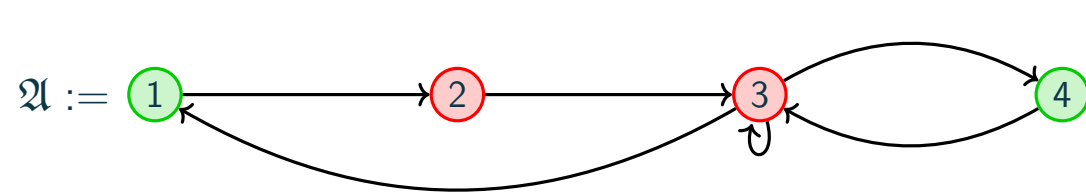
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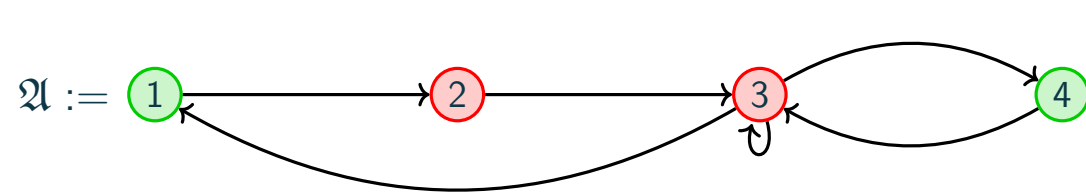
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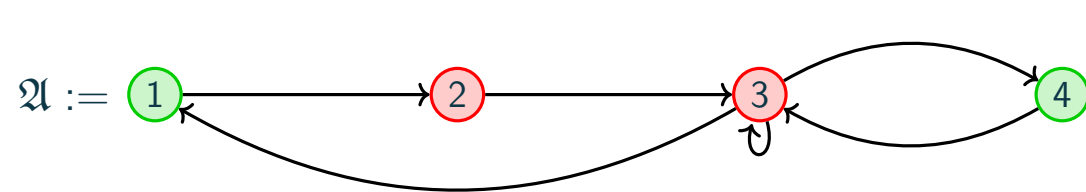
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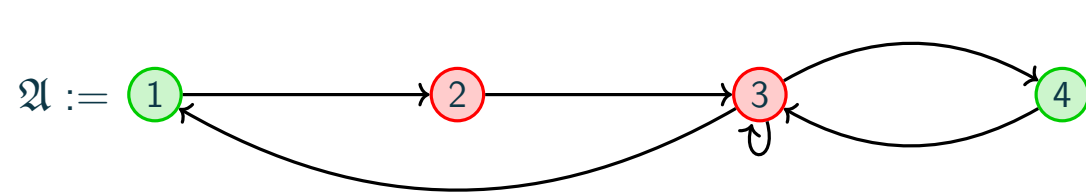
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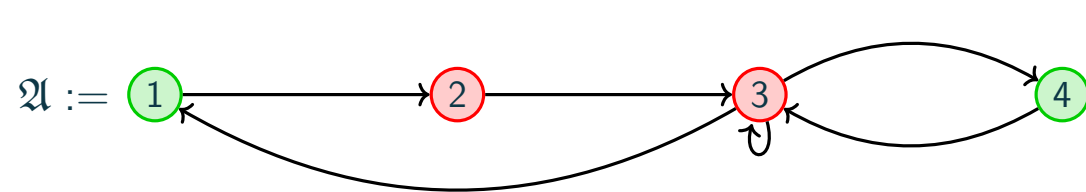
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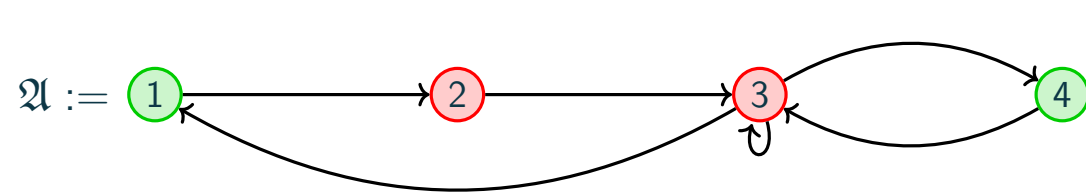
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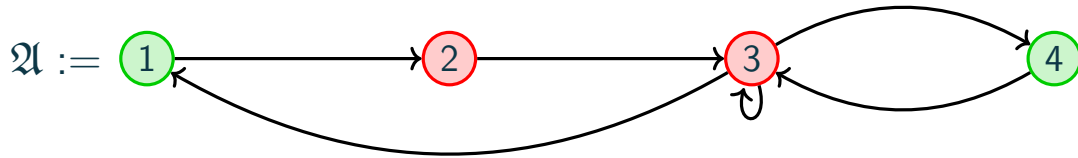
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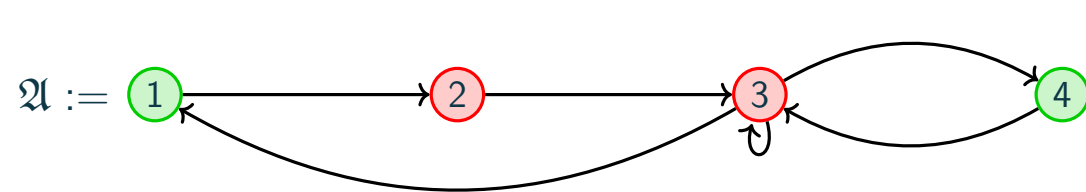
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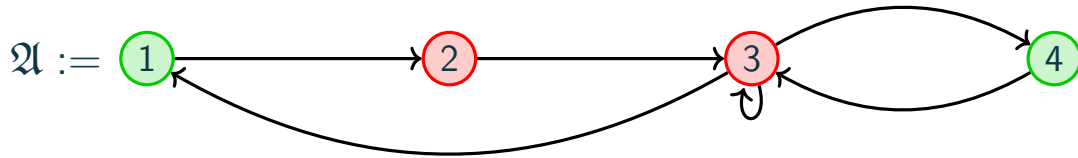
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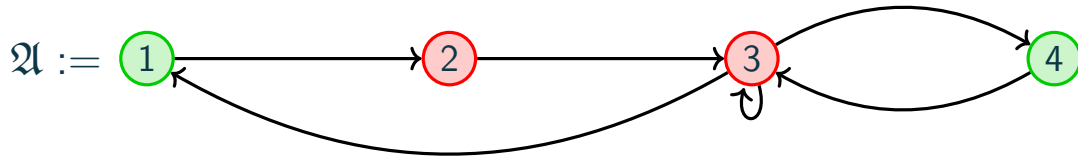
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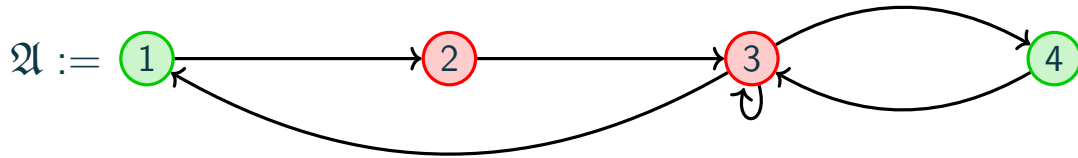
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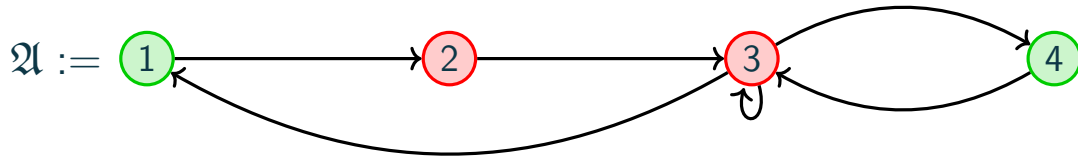
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(in a coloured graph:) Any node is either green or red.

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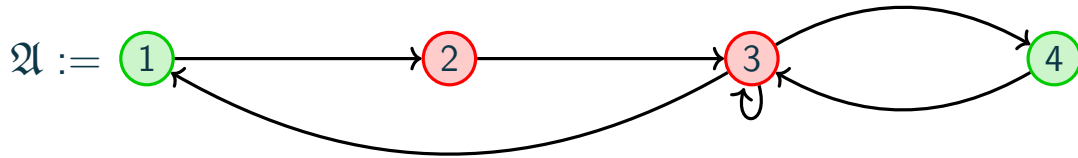
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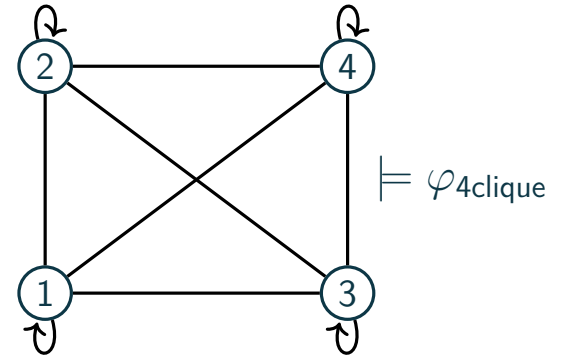


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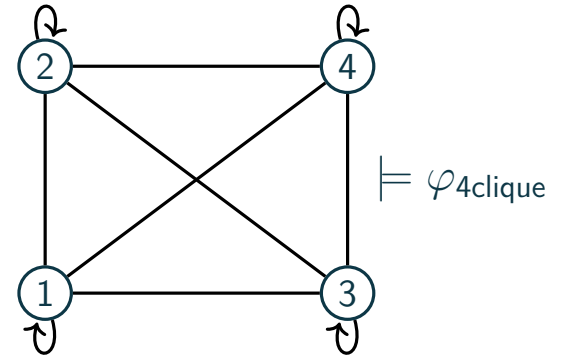
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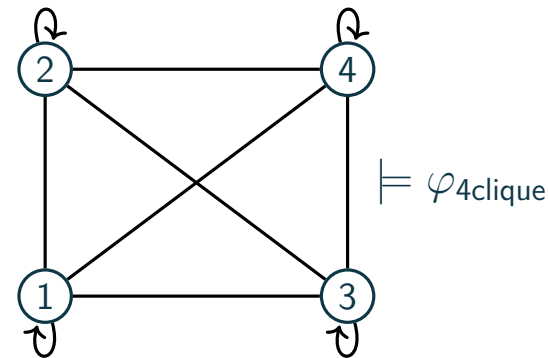


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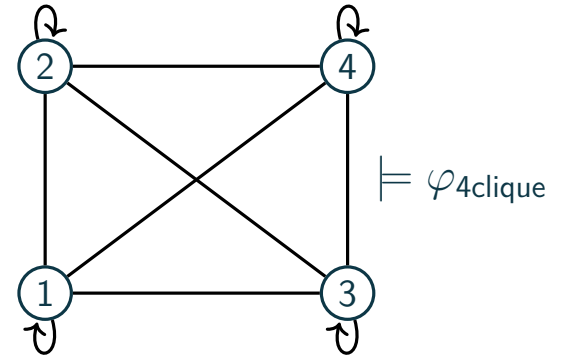
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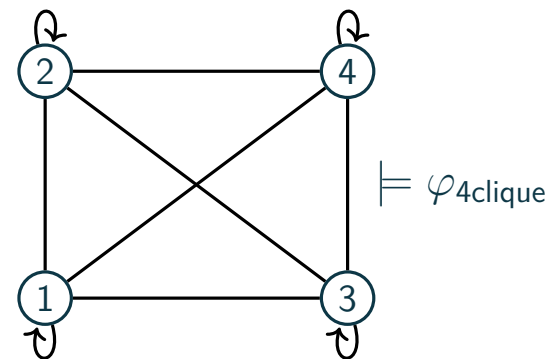
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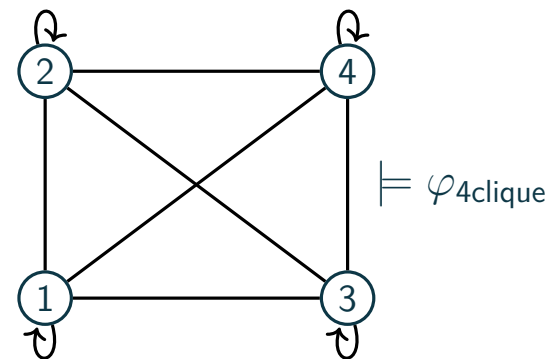
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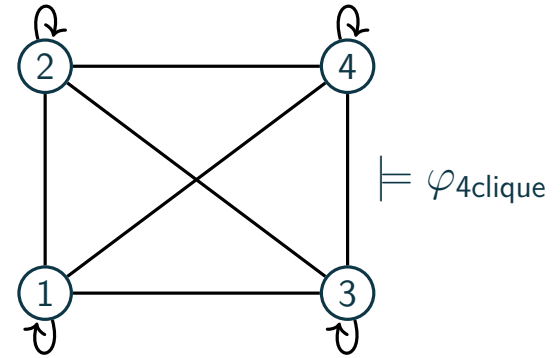
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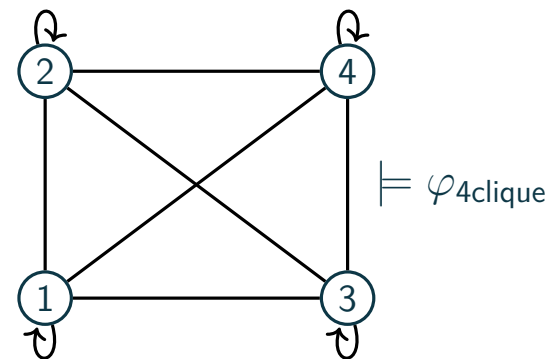
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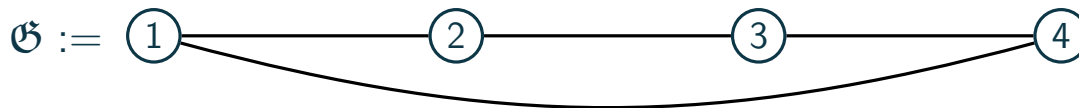
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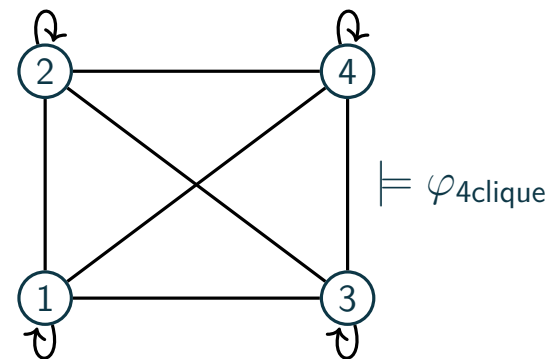
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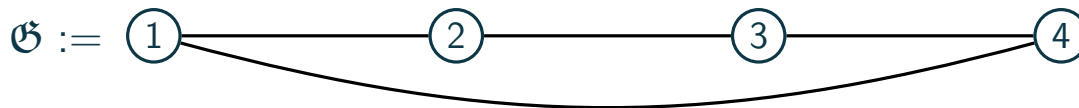
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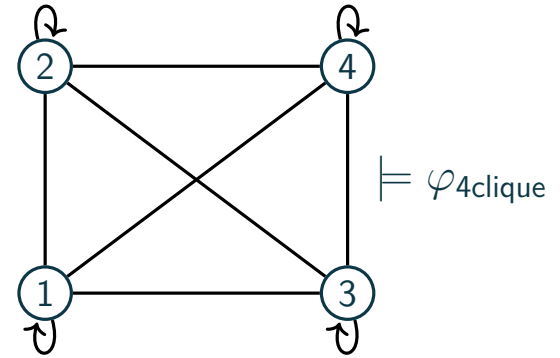
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Quantification over sets:



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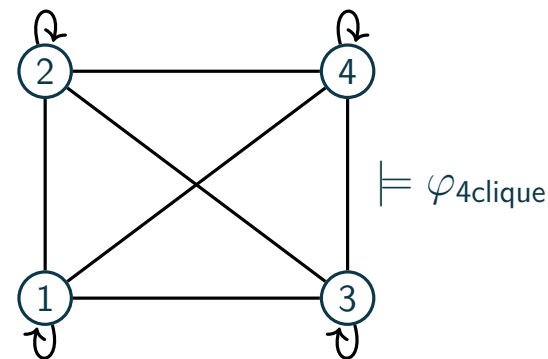
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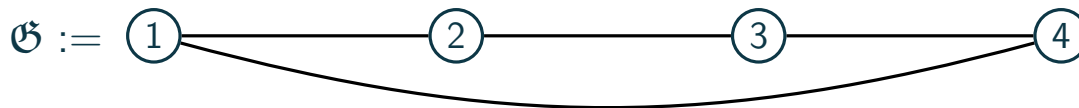
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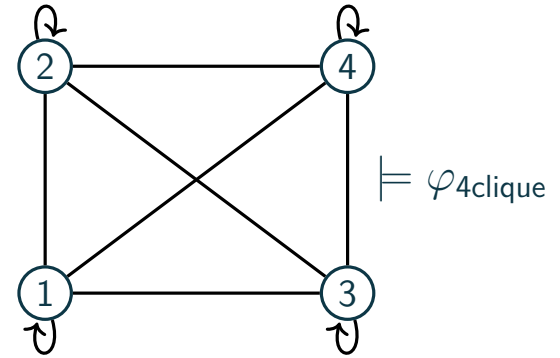
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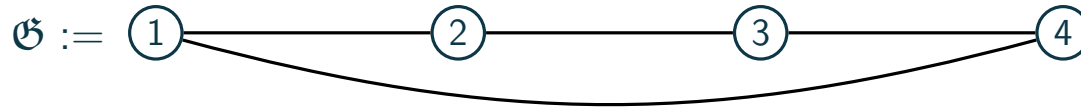
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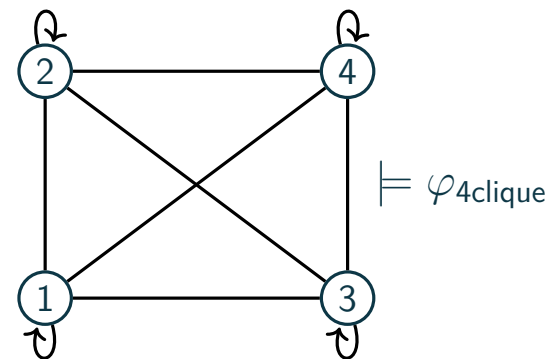
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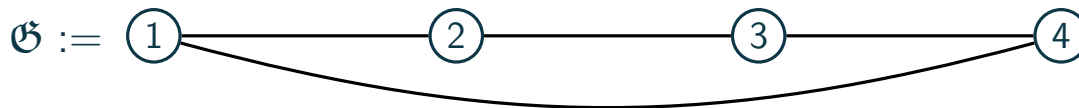
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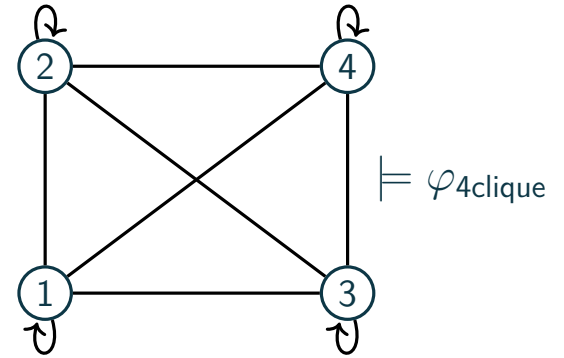
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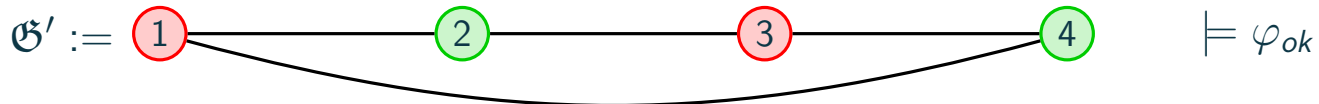


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No. And we will show it today!

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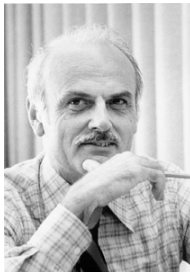
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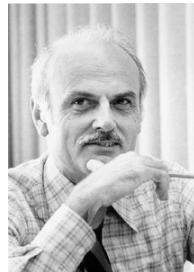
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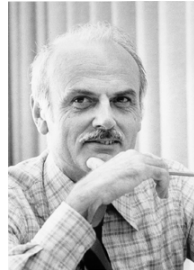
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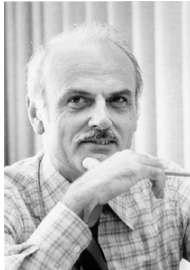
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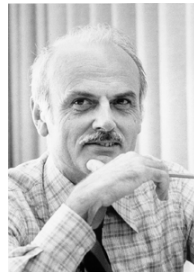
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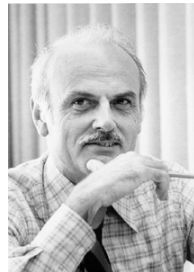
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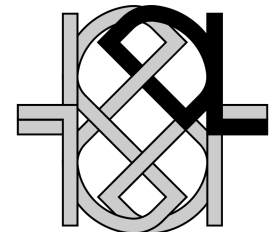
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Description logics: a family of logics for knowledge representation.

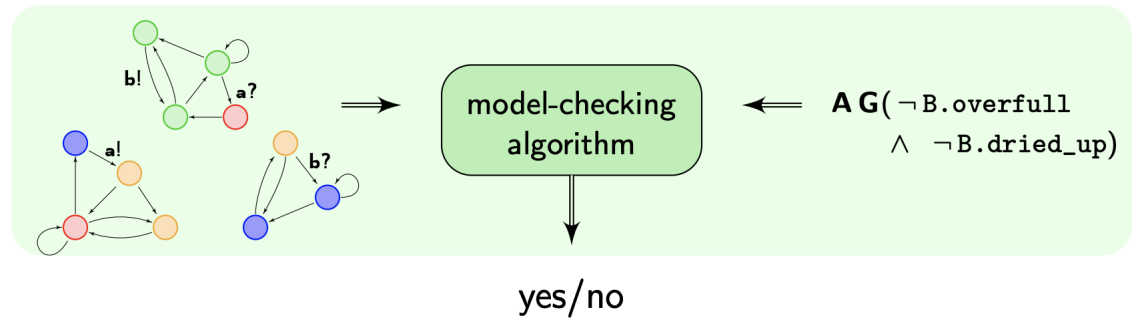
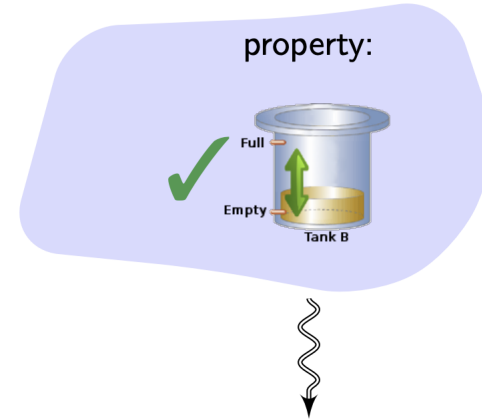
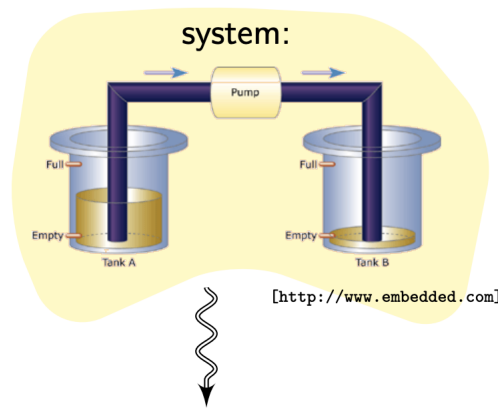


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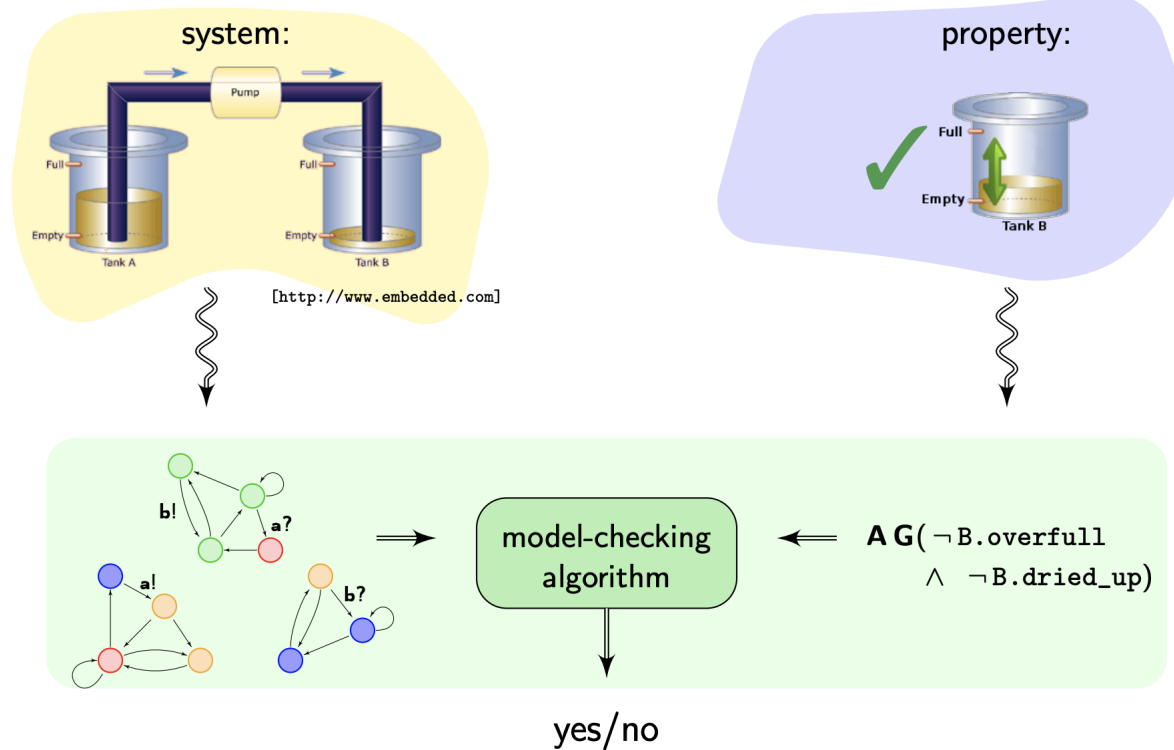
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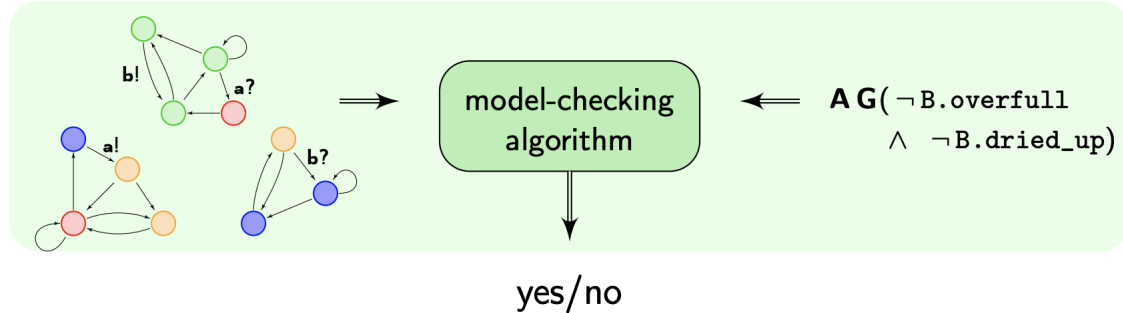
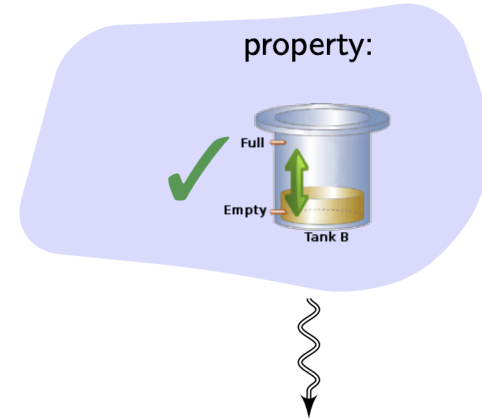
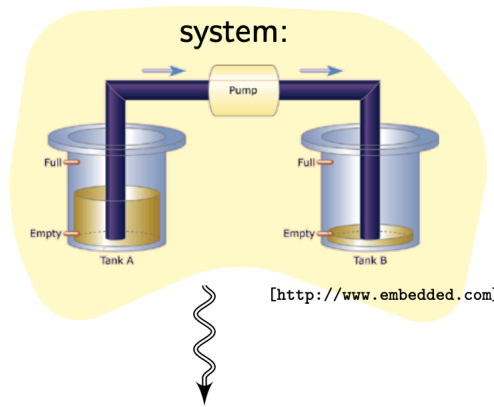
# Motivations II: why do we care about logic?

## 1. Temporal logics as specification languages



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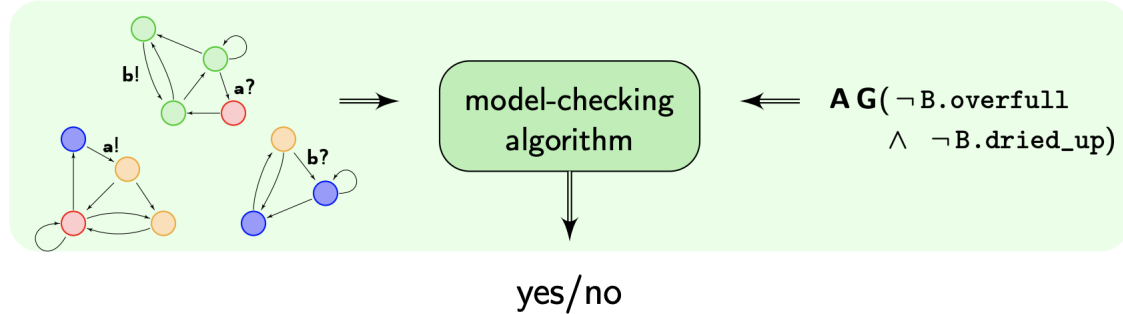
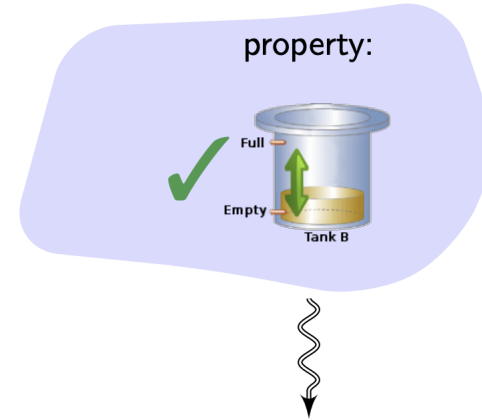
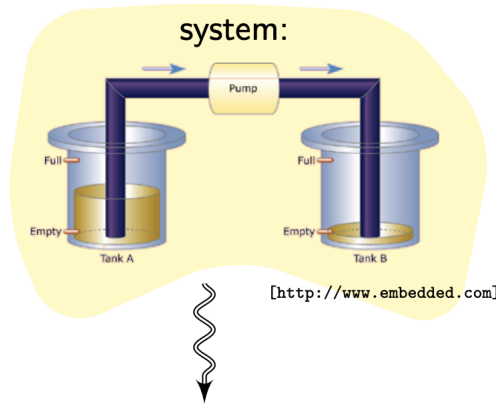
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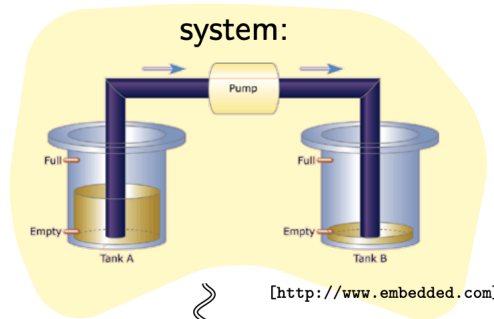
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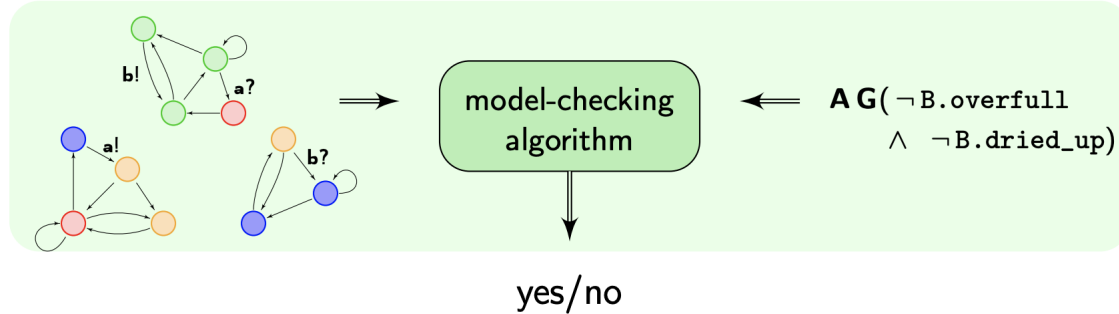
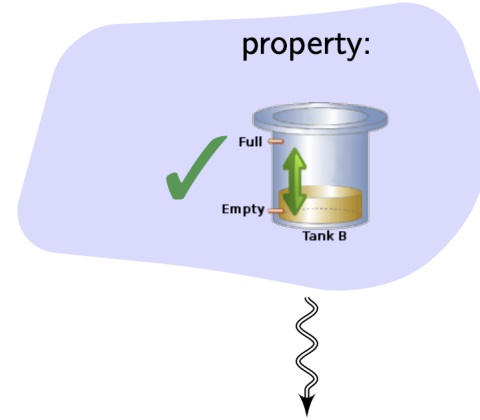
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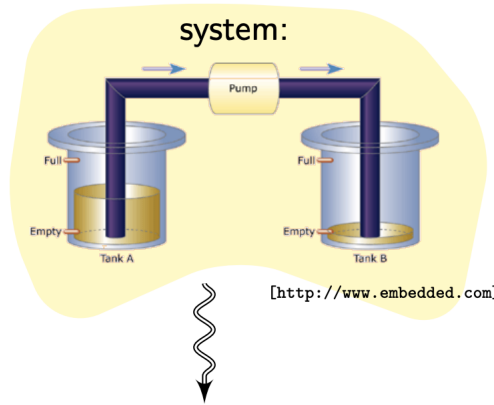
[<http://www.embedded.com>]



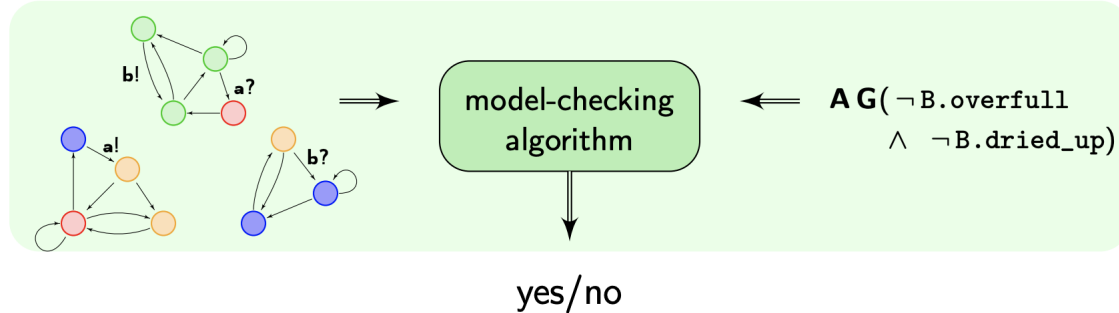
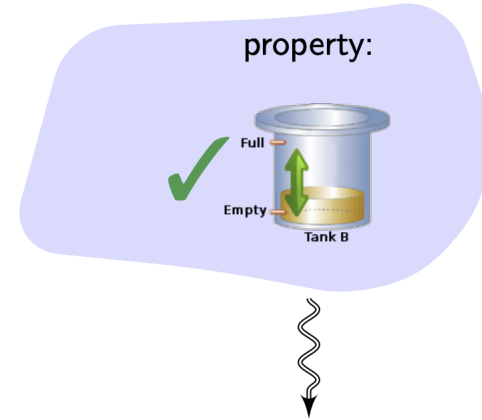
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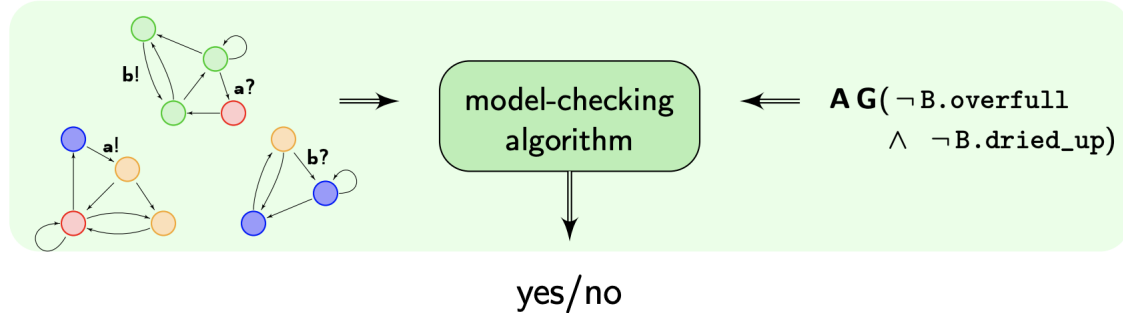
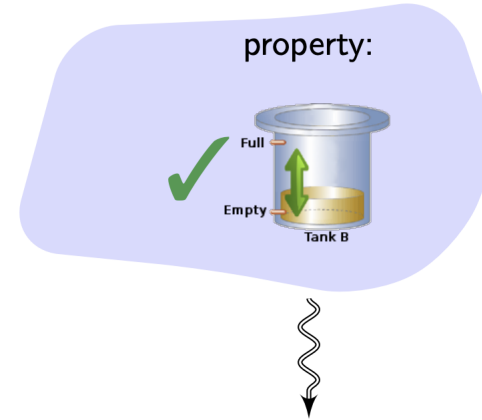
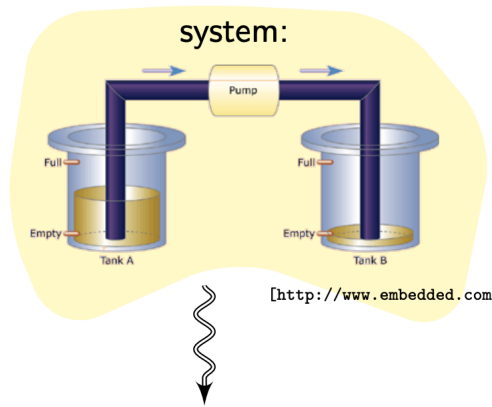


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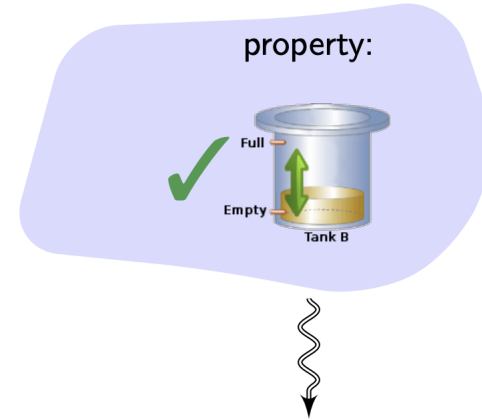
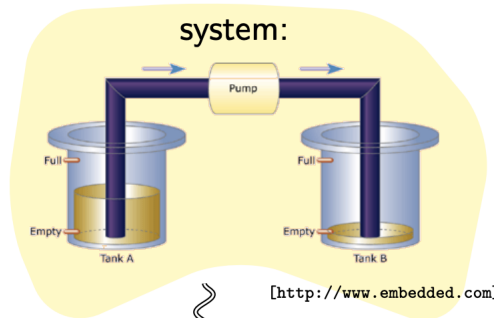


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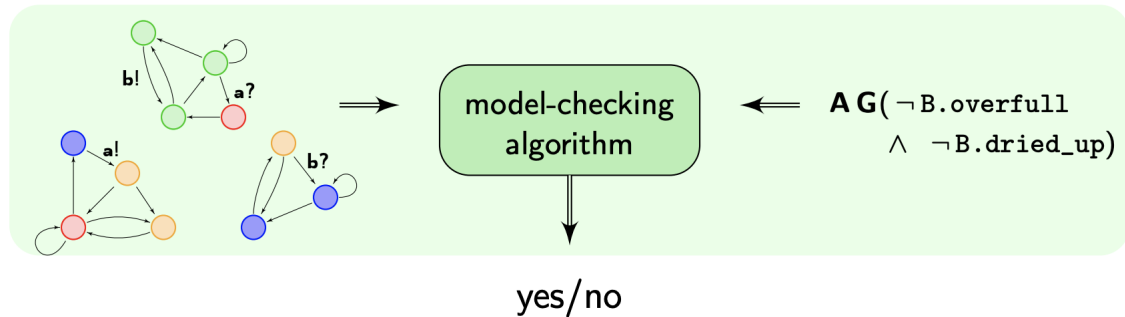
Nice lecture [here]. (I'm there running with a mic!)

Check also Infer tool by Facebook!



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vim hello.c
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#include <stdlib.h>

void test() {
    int *s = NULL;
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```

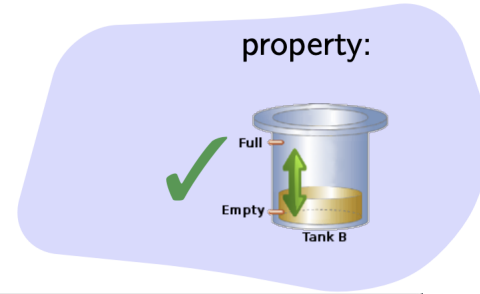
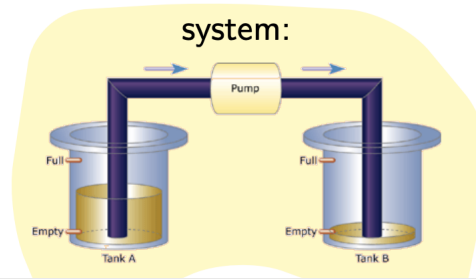


# Motivations II: why do we care about logic?

1. Temporal logics as **specification languages**
2. **COQ**: verified algorithms!, c.f. [here]
3. **Separation logic**: verifying Cpp/Java

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```
bartoszbednarczyk@Minsky-Machine: ~/Downloads/Infer
$ infer run -- gcc -c hello.c

Capturing in make/cc mode..
Found 1 source file to analyze in /Users/bartoszbednarczyk/Downloads/Infer/infer-out

Analysis finished in 775ms

Found 1 issue

hello.c:6: error: NULL_DEREFERENCE
  pointer `s` last assigned on line 5 could be null and is dereferenced at line 6, column 3.
4.   void test() {
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6. >  *s = 42;
7.   }

Summary of the reports

NULL_DEREFERENCE: 1
```

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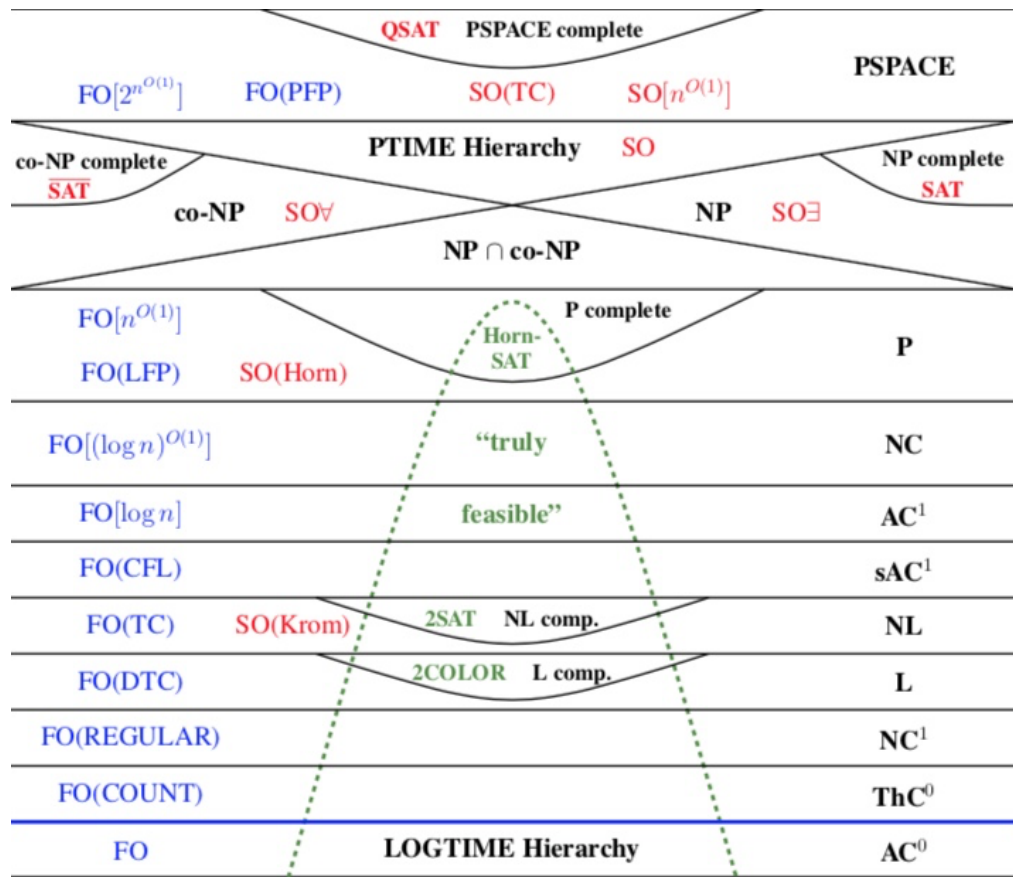
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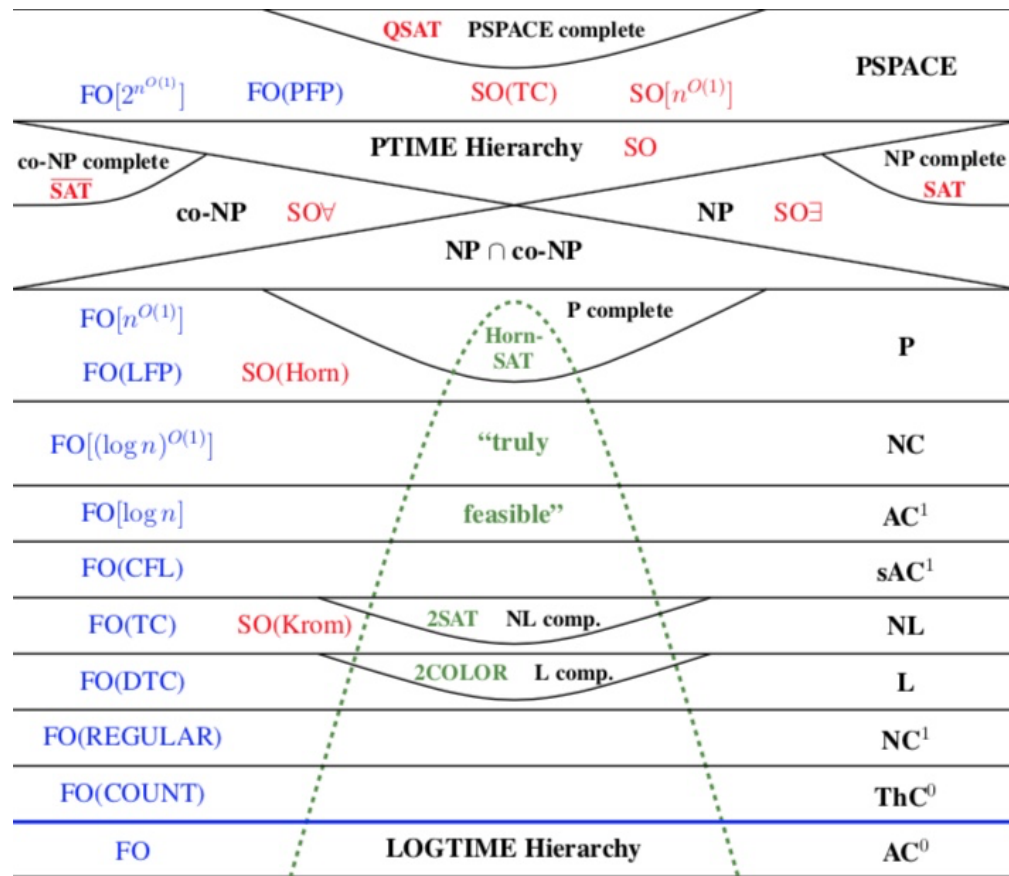


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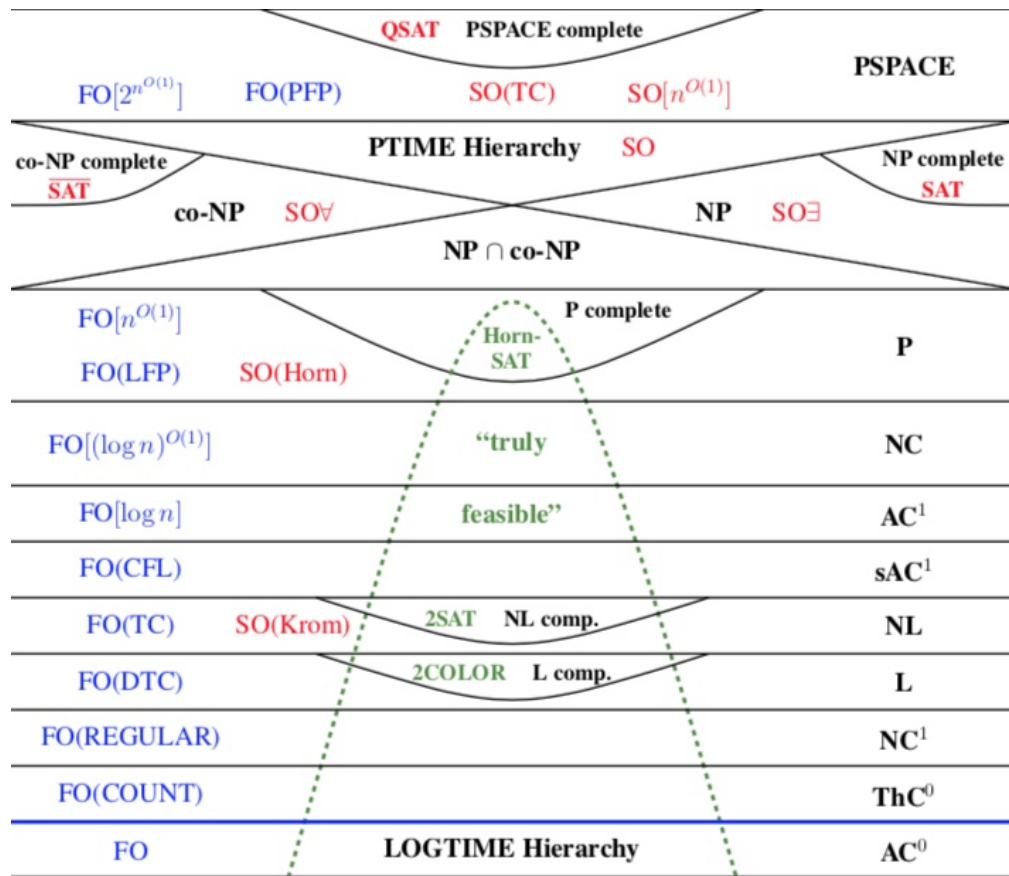
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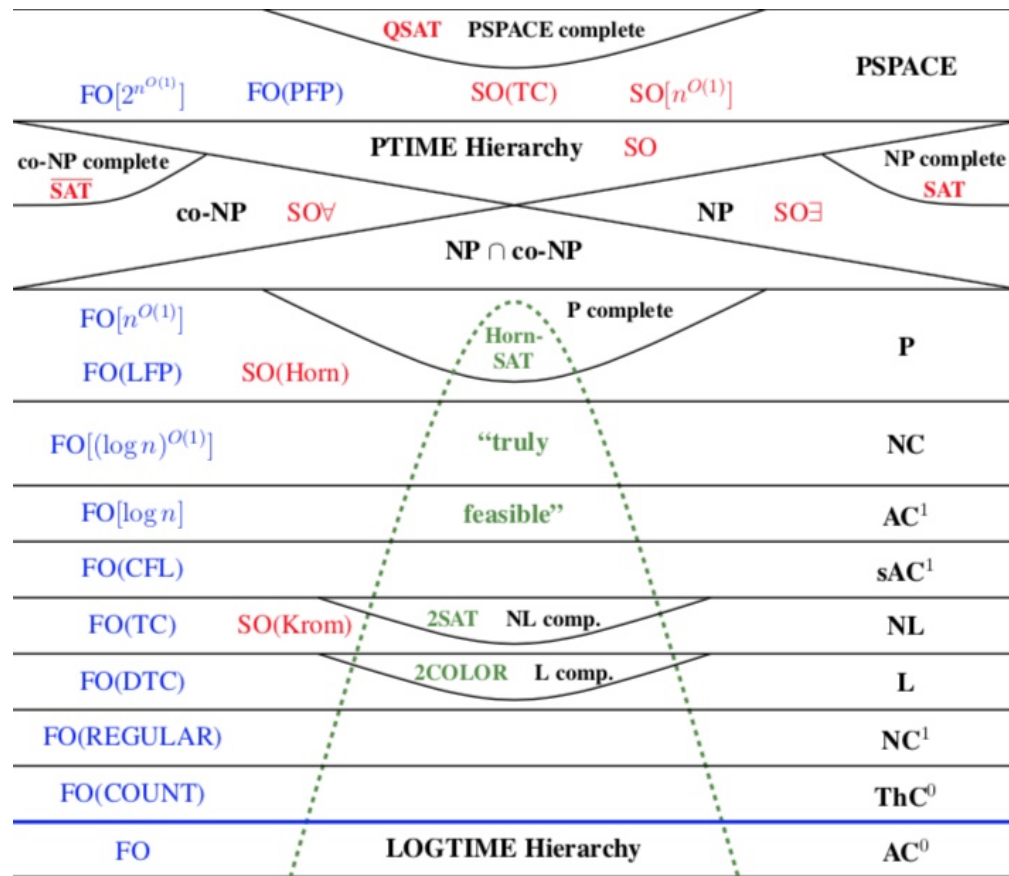
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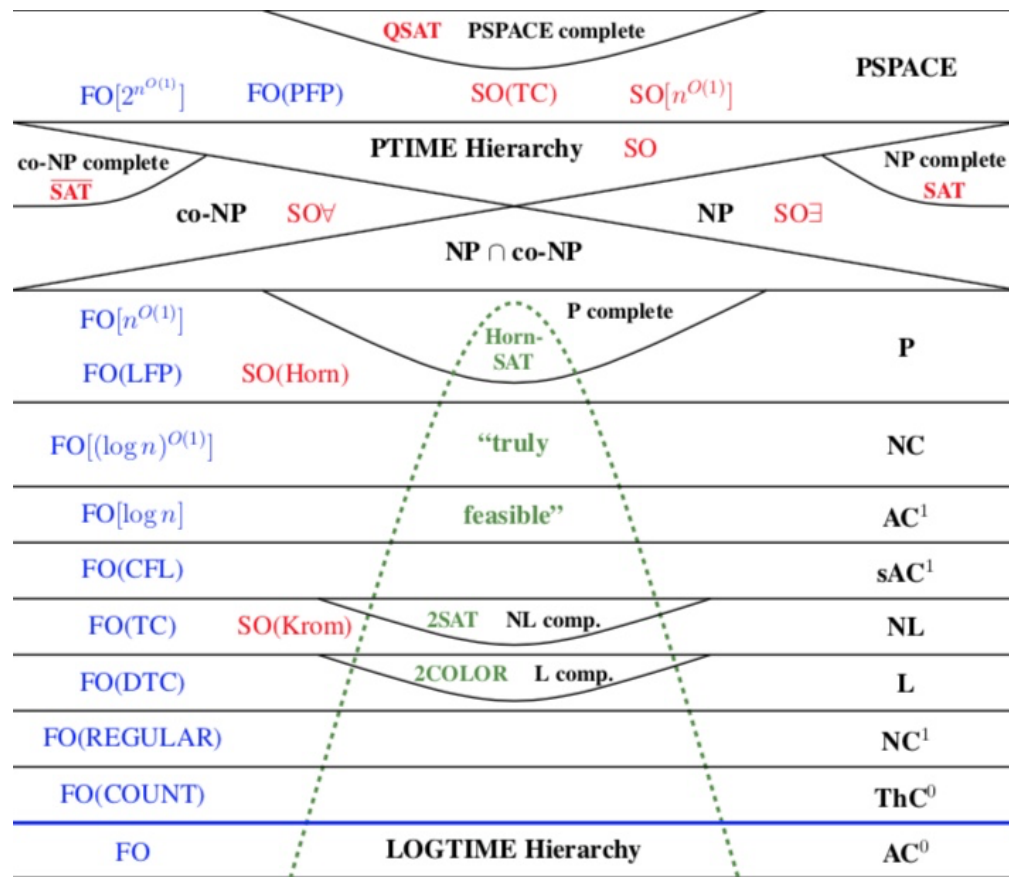
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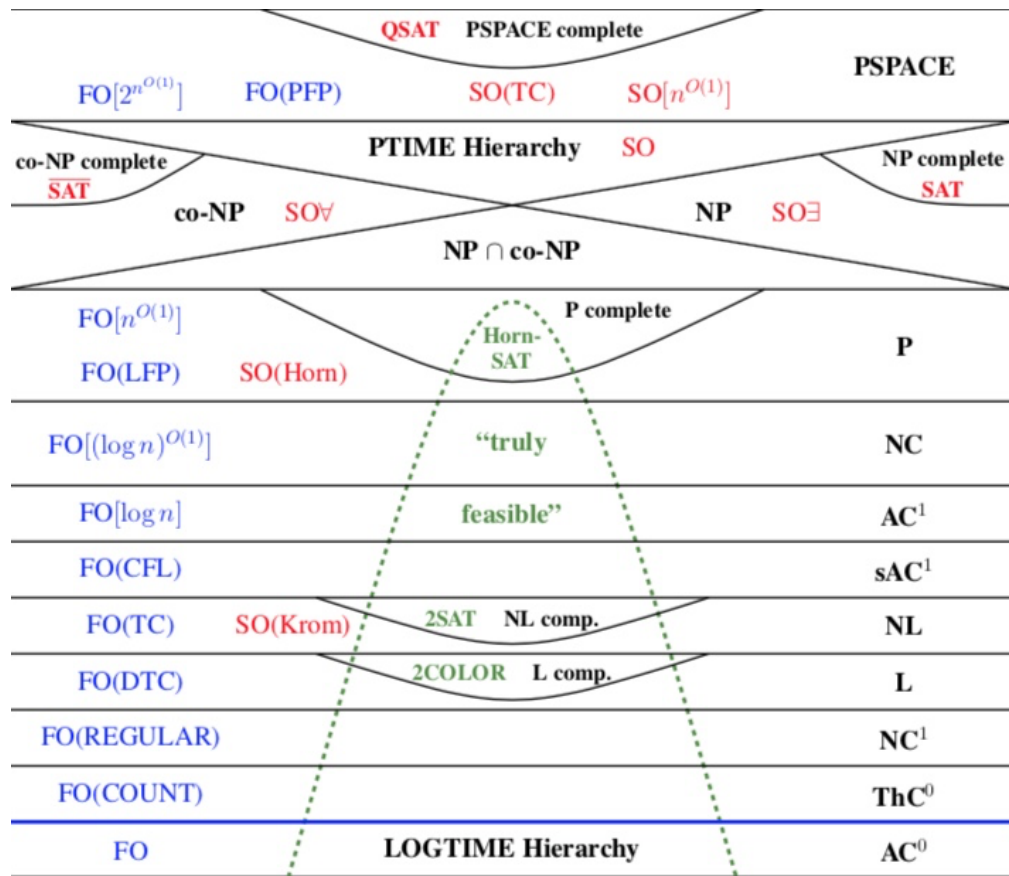
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Is there a logic for PTIME?

No idea since 1988.



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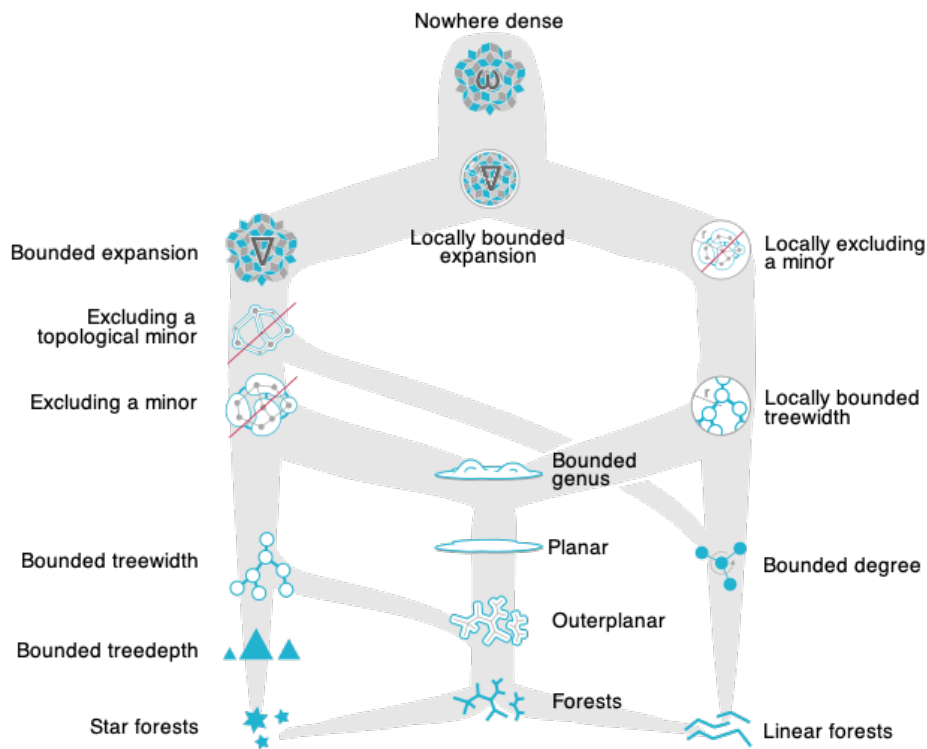
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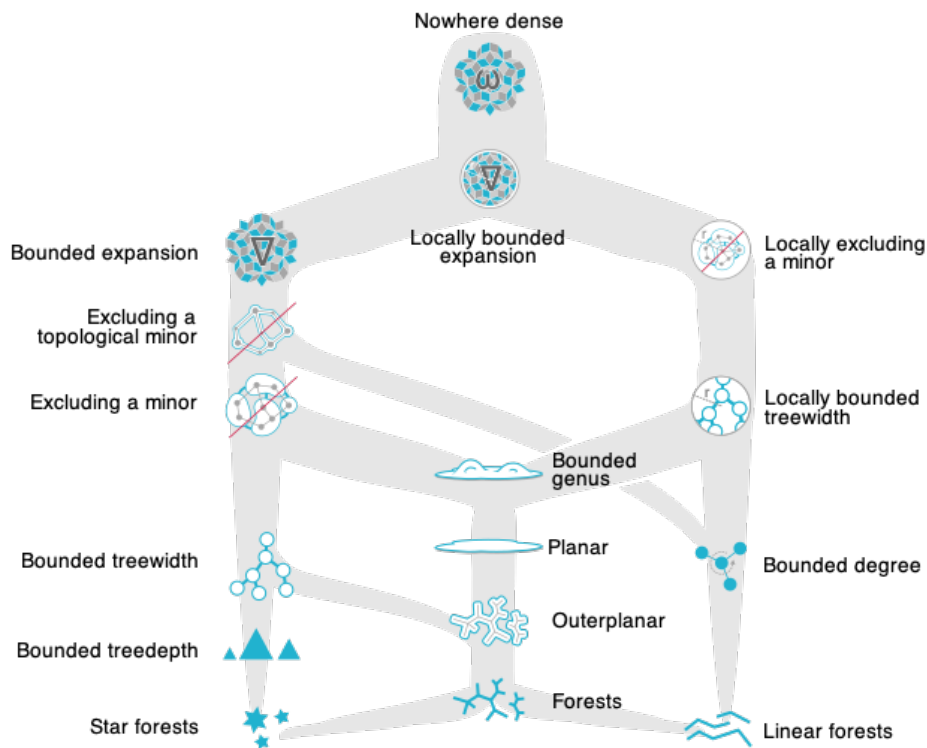
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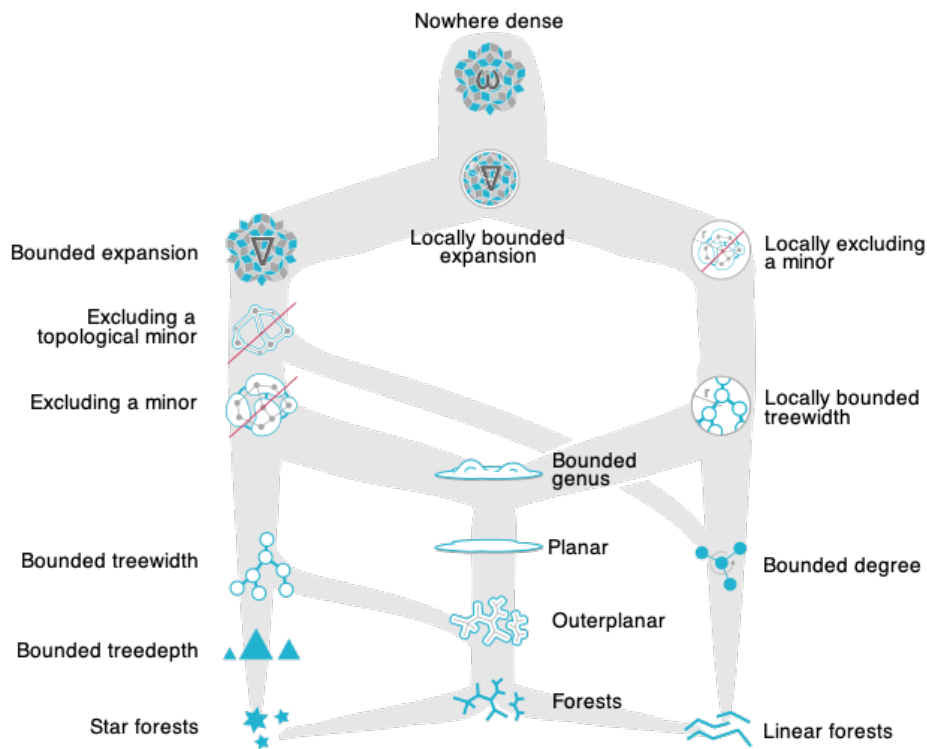
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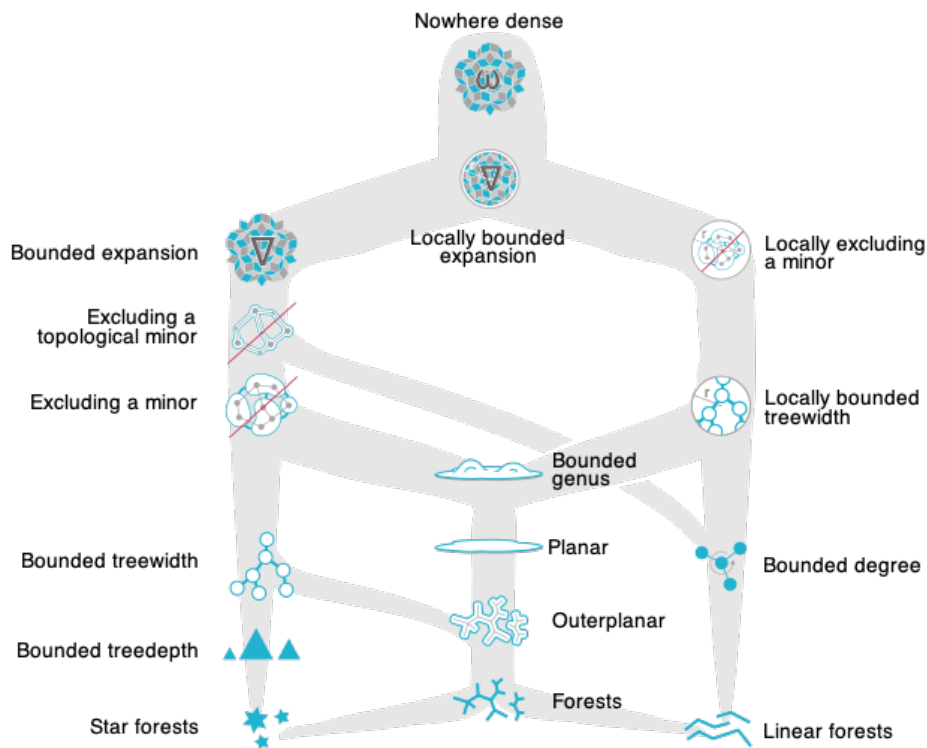
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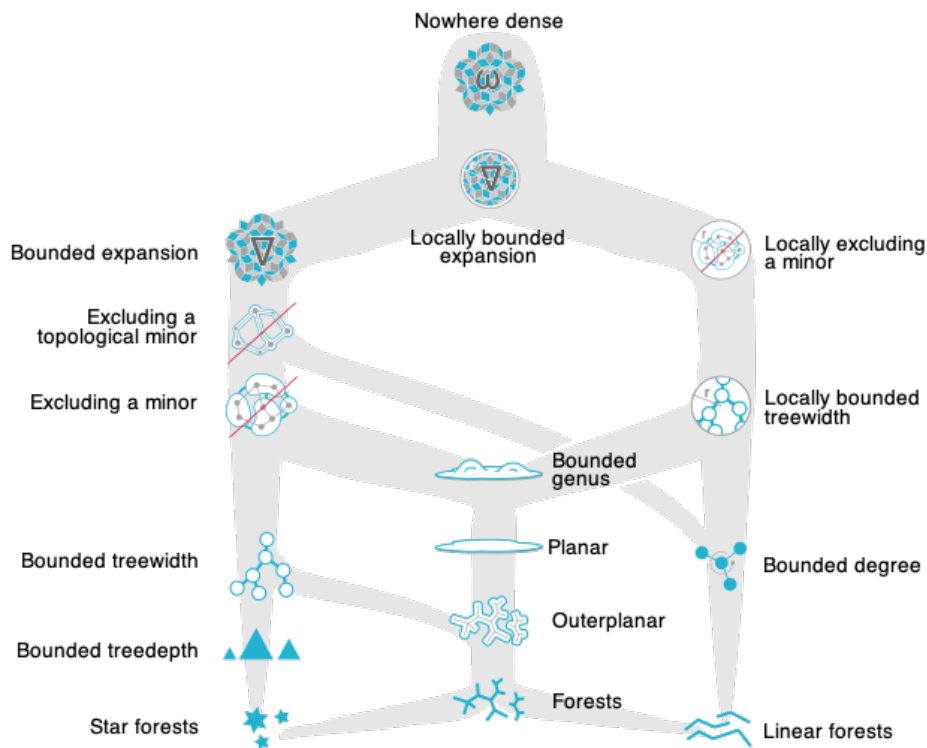
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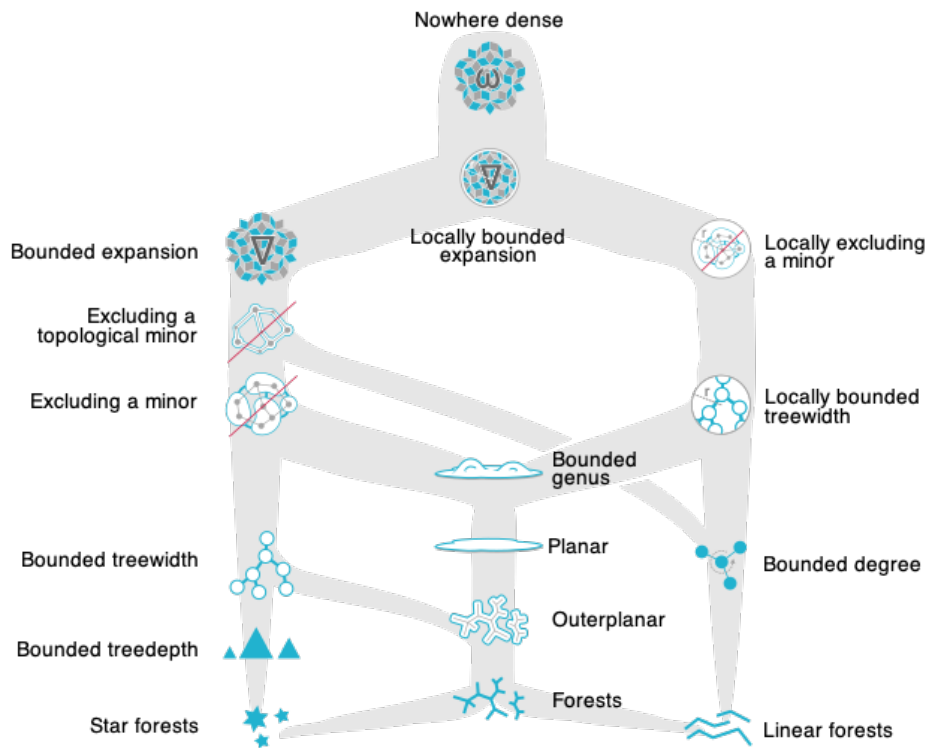
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**Theorem (Grohe, Kreutzer, Siebertz 2014)**

$O(|\varphi|^{1+\varepsilon})$  for  $\mathcal{C} :=$  nowhere-dense graphs.



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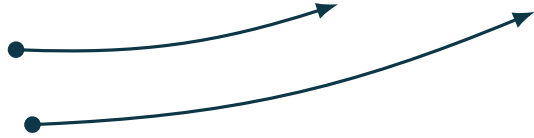


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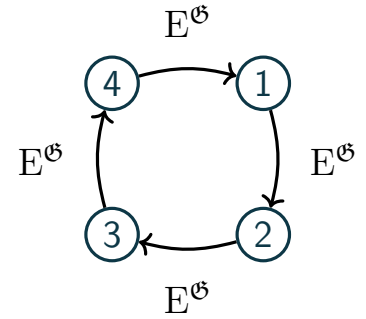
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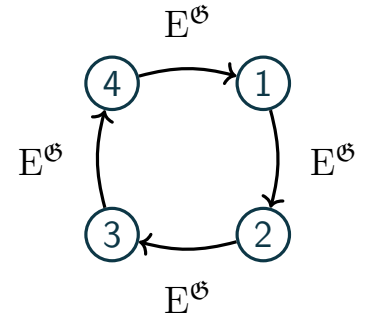
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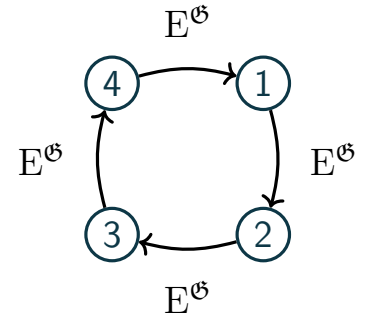
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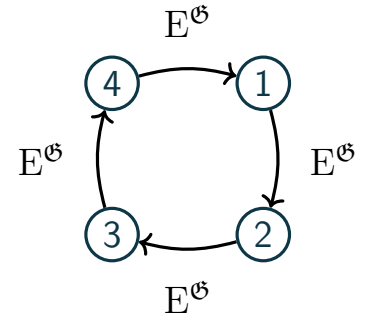
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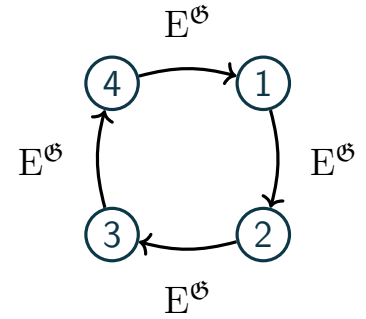
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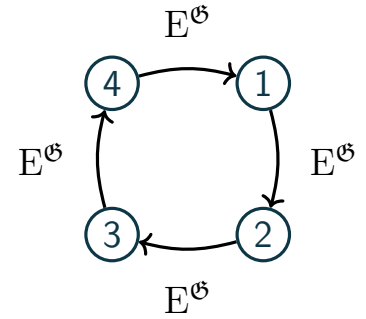
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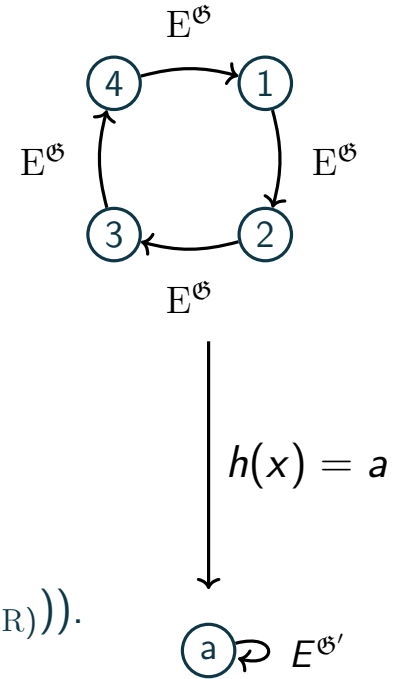
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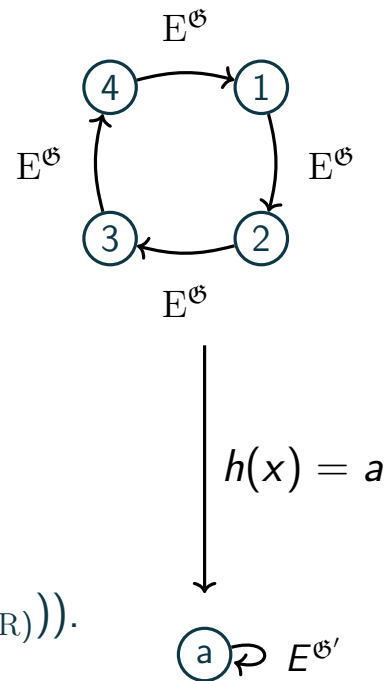
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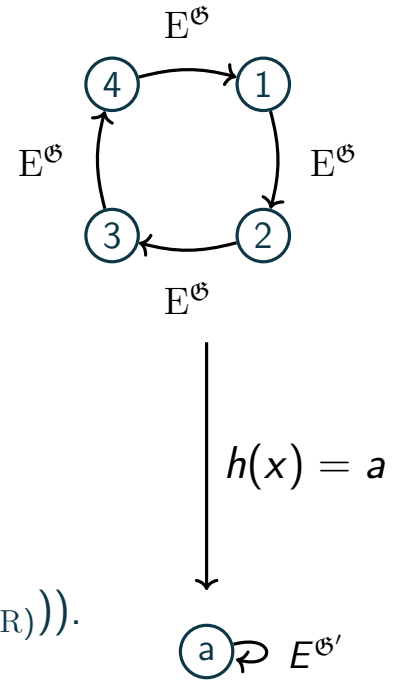
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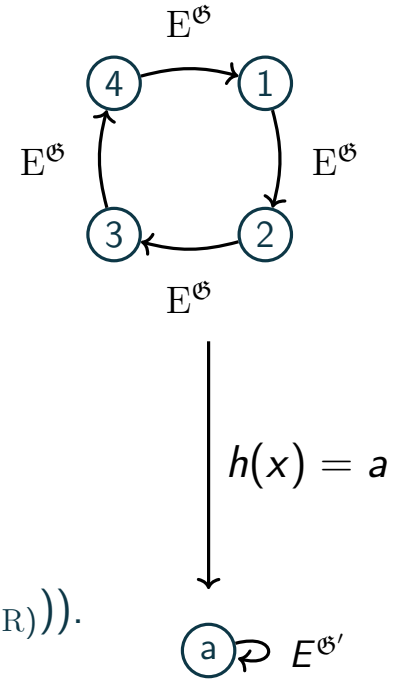
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- $\text{FVar}(\neg\varphi) = \text{FVar}(\varphi)$  and  $\text{FVar}(\varphi \wedge \psi) = \text{FVar}(\varphi) \cup \text{FVar}(\psi)$ . (and similarly for  $\rightarrow, \leftrightarrow, \vee, \top, \perp$ )
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## The last bunch of notations. Proof systems.

A formula  $\varphi$  is **satisfiable** if it has a **model** (there is a structure  $\mathfrak{A}$  s.t.  $\mathfrak{A} \models \varphi$ ).

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Warning! Models can be of any size: finite, countably-infinite and larger!  
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# The Gödel's Compactness Theorem

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Use case:  
Showing  
inexpressivity

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### 1st excursion: Proving (1)

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Craft  $\mathcal{T}_0$



# The Gödel's Compactness Theorem



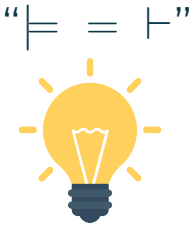
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Thus  $\mathcal{T}_0 \vdash \varphi$  holds (use the same proof as before!). After asking Gödel about “ $\models = \vdash$ ” again we are done.

## 2nd excursion: Proving (2)

# The Gödel's Compactness Theorem



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Thus  $\mathcal{T}_0 \vdash \varphi$  holds (use the same proof as before!). After asking Gödel about “ $\models = \vdash$ ” again we are done.

## 2nd excursion: Proving (2)

Ad absurdum



# The Gödel's Compactness Theorem



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Craft  $\mathcal{T}_0$

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Towards a contradiction suppose  $\mathcal{T}$  is unsatisfiable.

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$\mathcal{T}$  unSAT iff  $\mathcal{T} \models \perp$



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# Employing compactness I: Reachability in $\{E\}$ -structures

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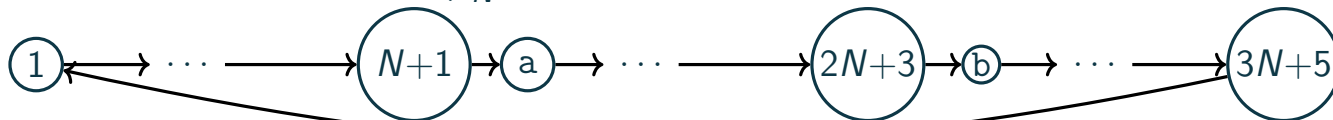
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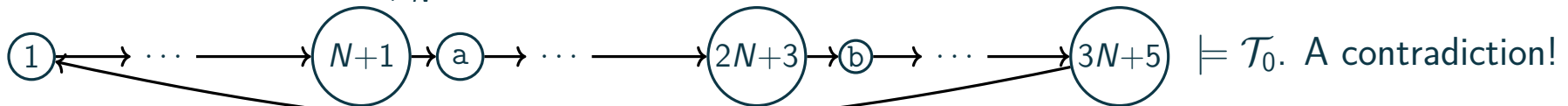
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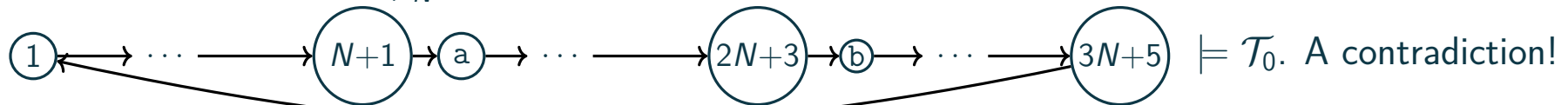
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Exploit  $\infty$ !

Let  $\lambda_k$  say “there are  $\geq k$  elem.”.



Löwenheim–Skolem!

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The previous proof does not give us **any information** about the **finite domain reasoning**.

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By  $\mathfrak{A} \models \mathcal{T}_1$  we get  $\mathfrak{A} \models \varphi$ . A **contradiction** (with the semantics of  $\models$ )!



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